

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant(s): Andrew J. Thurston  
Assignee: CISCO TECHNOLOGY, INC.  
Title: Automatic Generation Of Hardware Description Language Code  
For Complex Polynomial Functions  
Serial No.: 09/822,713 Filing Date: March 30, 2001  
Examiner: Unknown Group Art Unit: 2133  
Docket No.: CIS0061US

Austin, Texas  
August 30, 2004

COMMISSIONER FOR PATENTS  
P. O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying PTO Form-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on August 30, 2004.

  
Attorney for Applicant(s)

8/30/04  
Date of Signature

Respectfully submitted,



Marc R. Ascolese  
Attorney for Applicant(s)  
Reg. No. 42,268  
512-439-5085  
512-439-5099 (fax)

U.S. Department of Commerce, Patent and Trademark Office						Atty Docket No.		Serial No.	
						CIS0061US		09/822,713	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT						Applicant(s)			
(Use several sheets if necessary)						Andrew J. Thurston			
						Filing Date		Group	
						March 30, 2001		2133	

  

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	4,597,083	June 24, 1986	Stenerson	371	37	
	AB	4,873,688	Oct. 10, 1989	Maki et al.	371	37.1	
	AC	5,051,999	Sep. 24, 1991	Erhart et al.	371	41	
	AD	5,363,379	Nov. 8, 1994	Eckenrode et al.	371	3	
	AE	5,574,717	Nov. 12, 1996	Tomizawa et al.	370	13	
	AF	5,583,499	Dec. 10, 1996	Oh et al.	341	94	
	AG	6,199,188	Mar. 6, 2001	Shen et al.	714	782	
	AH	6,385,751	May 7, 2002	Wolf	714	784	
	AI	6,683,855	Jan. 27, 2004	Bordogna et al.	370	244	
	AJ	6,684,350	Jan. 27, 2004	Theodoras, II et al.	714	712	
	AK	6,751,743	June 15, 2004	Theodoras, II et al.	713	400	

  

Patent Application Publication							
		Publication Number	Publication Date	Name	Class	Subclass	Filing Date If Appropriate
	AL	2002/0165962	Nov. 7, 2002	Alvarez et al.	709	226	
	AM						

  

Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AN							
	AO							
	AP							

  

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
	AQ	Christian Schuler, "Code Generation Tools For Hardware Implementation Of FEC Circuits," GMD FOKUS, Kaiserin-Augusta-Allee 31, 10589 Berlin, Germany.
	AR	
	AS	

  

Examiner	Date Considered
----------	-----------------